	Specification	S	ymbol	Ol Condition / Comment						HTS 201-10 LC2 Unit		
	Maximum Operating Voltage		O(max)	I <sub>off</sub> < 50 μADC, T <sub>case</sub> = 70°C						20	kVDC	
	Maximum Operating Voltage  Maximum Isolation Voltage		O(max)	Between HV switch and control / GND, continuously				nucly		· · · · · · · · · · · · · · · · · · ·	kVDC	
60	ů		INS						+	± 40	kVDC	
RATINGS	Max. Housing Insulation Voltage  Maximum Turn-On Peak Current			Between switch and housing surface, 3 minutes $T_{case} = t_p < 200 \mu s$ , duty cycle < 1%					+	± 40 100	KVDC	
	Maximum Turn-On Peak Current		P(max)	T <sub>case</sub> = 25°C	t <sub>p</sub> < 200 μs, t <sub>p</sub> < 1 ms, du					59	ADC	
2				25 C						36	ADC	
				t <sub>p</sub> < 10 ms, duty cycle						27		
MAXIMUM				_	t <sub>p</sub> < 100 ms, duty cycle <1%						100	
3	Maximum Continuous Load Current		I <sub>L(max)</sub>	T <sub>case</sub> = Standard devices						0.85	ADC	
3	N 0 " B B' : "		D	25°C Devices with option DLC						8.25	ļ	
-	Max. Continuous Power Diss	sipation Po	$P_{d(max)}$	T <sub>case</sub> = Standard devices & FC, forced air 4 m/s 25°C Devices with option DLC						13		
ABSOLUTE				25°C						1200	Watt	
7	Linear Derating			Above Standard devices & FC, forced air 4 m/s						0.28	10///	
20				25°C Devices with option DLC						26	W/K	
48	Operating Temperature Range		0	Standard devices & options CF, GCF, ILC. (Option DLC)						-4070	C°	
	Storage Temperature Range		S	Switches with option ILC may require frost protection!						-4090	C°	
	Max. Permissible Magnetic Field			Homogeneous steady-field, surrounding the whole switch				e switch		25	mT	
	Max. Auxilliary Voltage		aux	Built-in overvoltage limiter (replaceable)						5	VDC	
	Permissible Operating Voltage Range		'o							0 ± 20	kVDC	
	Typical Breakdown Voltage		br	NOTE: V <sub>br</sub> is	a test param	eter for qu	ality Ioff	> 0.5 mA		22	kVDC	
	Typical Off-State Current		ıff	control purposes only. Not applicable in loff > 0.5 mA   0.8xVo, T <sub>case</sub> = 2570°C, reduced l <sub>off</sub> on request				+	+-	20	μADC	
	Typical Turn-On Resistance		stat							7.8	μλυσ	
	Typical Fulli-Off Resistance		stat	Each switching path  0.1 x I <sub>P(max)</sub> , T <sub>case</sub> =25°C					9.1			
				$t_p < 1\mu s$ , duty cycle < 1% 1.0 x $I_{P(max)}$ , $T_{case} = 25^{\circ}C$					19	Ohm		
	Typical Propagation Delay Time		, ,	1.0 x I <sub>P(max)</sub> , T <sub>case</sub> =70°C					+-	250		
	Typical Propagation Delay Time		l(on)	Resistive load, 0.1 x I <sub>P(max)</sub> , 0.8 x V <sub>O(max)</sub> , 50-50%					+	3	ns	
	Typical Output Pulse Jitter			Impedance matched input, $V_{aux} / V_{ctrl} = 5.00 \text{ VDC}$ Resistive load, 10-90%						-	ns	
	Typical Turn-On Rise Time		$t_{r(on)}$	Resistive id	oad, 10-90%		$0.1 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$			12		
							$0.8 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$			22		
				5	1 10 000/			$O(max)$ , $I_L = 1.0 \times I_{p(max)}$		25	ns	
S	Typical Turn-Off Rise Time	t <sub>of</sub>	ıff, tq				$0.1 \times V_{O(max)}$ , $I_L = 0.1 \times I_{p(max)}$			30	ns	
12				N. 11 11 11 11				$L = 1.0 \times I_{p(max)}$		80	<u> </u>	
CTERISTICS	Maximum Turn-On Time		n(max)	No limitation						∞	ļ	
E.	Minimum Turn-On Time		n(min)	ton(min) can be customized. Please consult factory				<u>/</u>		150	ns	
2	Maximum Turn-Off Time		ff(max)	No limitation						∞		
CHARA	Minimum Turn-Off Time	ff(min)	toff(min) can be customized. Please consult factory						150	ns		
¥	Max. Continuous Switch	ing f <sub>(n</sub>	max)	@ V <sub>aux</sub> = 5.00 V Standard devices without HFS option						6		
Ö	Frequency			•			I devices with I			100		
44					Opt. HFS + sufficient cooling option					200	kHz	
ELECTRICAL	Maximum Burst Frequency		(max)	Use option HFB for >10 pulses within 20µs or less				SS		2	MHz	
K	Maximum Number of Pulses / Burst		(max)	@ f <sub>b(max)</sub> Standard				Standard		10	Pulses	
EC				Note: Option HFE	3 requires external bu	uffer capacitors	with a voltage	Option I-HFB		>100		
EL				rating of > 630VDC and a cpacitance of 100nF per additional pulse.  Option HFB						>10000		
	Coupling Capacitance		c	HV side against control side						<100	pF	
	Natural Capacitance		N	Between switch poles, @ 0.5 x V <sub>O(max)</sub>						26	pF	
	Control Voltage Range		ctrl	The V <sub>ctrl</sub> has no impact on the output pulse shape.				pe.		3 10	VDC	
	Auxiliary Supply Voltage Range		aux	The +5 V s	upply is not r	equired ir	the HFS mod	e.		5	VDC	
	Typical Auxiliary Supply Current		IUX	V <sub>aux</sub> = 5.00 VDC, T <sub>case</sub> = 25°C. 0.01 x f <sub>(max)</sub>				0.01 x f <sub>(max)</sub>		170		
				Active current limitation above 1A. @ f <sub>(max)</sub>					800	mADC		
	Fault Signal Output			Switch will be turn off, if f>f <sub>(max)</sub> , V <sub>aux</sub> <4.75V or T <sub>case</sub> >75°C				<sub>case</sub> >75°C		H=4V, L=0.5V	VDC	
				Fault condition is indicated by a logical "L"								
	Opt. HFS, Ext. Supply Voltage V1		HFS(V1)	Stability ±3%, current consumption <0.4 mA/kHz @ 25°C						15	VDC	
	Opt. HFS, Ext. Supply Voltage V2		HFS(V2)	Stability ±3%, current consumption <0.9 mA/kHz @ 25°C				z @ 25°C		101	VDC	
	Intrinsic Diode Forward Voltage		F	$T_{case} = 25^{\circ}C$ , $I_F = 0.3 \times I_{P(max)}$						<26	VDC	
	Diode Reverse Recovery Time		rc	$T_{case} = 25$ °C, $I_F = 0.3 \times I_{P(max)}$ , $di/dt = 100 \text{ A/}\mu\text{s}$						<250	ns	
	Dimensions		xWxH	Standard housing						200x75x56		
9				Devices with option CF, non-isolated cooling fins				S		Please contact the	mm <sup>3</sup>	
HOUSING				Devices with option DLC						manufactured!		
Ž	Weight			Standard housing						Please contact the		
Ħ			Devices with option CF, non-isolated cooling fins						manufactured!	g		
				Devices with option DLC								
	Control Signal Input	npatible with Schmitt-Trigger characteristics. Control voltage 2-10 V (3-5 V recommended for low jitter).										
	Logic GND / 5V Return   Pin 2 / Black. The ground pin is internally connected with the safety earthing terminal (threaded insert) on bottom side.											
S												
0	Fault Signal Output  Pin 4 / Orange. TTL output, short circuit proof. Indicating switch & driver over-heat, over											
FUNCTIONS												
Š												
F												
	Temperature Protection	d switches with option CF, GCF: Thermo trigger 75°C, response time < 60 s @ 3xPd(max), \( \Delta T = 25K \) (50 to 75°C). Separate driver										
			with option DLC: 65°C, response time < 3 s @ 3xPd(max), ΔT=25K (40 to 65°C), coolant flow > 3l / min. Separate driver protection.									
	HTS 201-10 LC2 Transistor Swit											
9				n S-TT Soft Transition Time. Slower switching speed for simplified EM n HFB High Frequency Burst, Improved burst capability by driver.					Ceramic Flange Housing. P <sub>d(max)</sub> can be increased by the factor 3 to 15 Copper Cooling Fins. P <sub>d(max)</sub> can be increased by the factor 3 to 10.	J.		
									•			
ORDERINGT				n UFTR Ultra Fast Thermotrigger. Response time for shut down < 5s. Option I					LC Indirect Liquid Cooling (for water). P <sub>d(max)</sub> can be increased by the factor 3 to 15.			
2	1						se time < 5s. NTC 1		on DLC	Direct Liquid Cooling (for FPE/PFC). Pd(max) can be increased by the factor 10 to		
1			0 0 0.0		i i dol i i i ci i i ooci i	oon recopon	00 11110 1 00:1110 1	0117 ± 170 Optio		Direct Educa Gooding (for 11 Ed 1 O). 1 q(max) can be increased by the lactor 10 t		
	omized switching units are availal			FOR	FURTHER PRO	DUCT OPTI	ONS PLEASE REF	FER TO THE OPTIO	ONS PAG	SE.		